

IN THE SPECIFICATION

Please amend the specification as follows:

The paragraph beginning at page 4, line 14 is amended as follows:

As shown in FIG. 2, the INT signal is a version of the SEND signal and the INT and SEND signal have the same frequency. The XSIG signal has a frequency higher than the frequency of the SEND and INT signals. In FIG. 2 the edges of the SEND, XSIG, and INT signals are aligned. However, offsets or misalignments may exits ~~exist~~ between the edges of the SEND, XSIG, and INT signals. The offsets may be caused by either one or both of a signal propagation delay time and a response time of circuit elements. For example, an offset may ~~exists~~ exist between the edges of the SEND signal and the INT signal at T1 and at T2 in which the edge of the INT signal may be delayed from the corresponding edge of the SEND signal by an offset value. In some embodiments, the offset value may be one or more cycles of the XSIG signal.